

DOCKET NO.: 240896US2S/hyc



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

IN RE APPLICATION OF: Naofumi NAKAMURA, et al.

SERIAL NUMBER: 10/628,372

GROUP: 2815

FILED: July 29, 2003

EXAMINER: WARREN, Matthew E.

FOR: SEMICONDUCTOR DEVICE HAVING MULTI-LAYER INTERCONNECTION  
STRUCTURE AND METHOD OF MANUFACTURING THE SAME

**REQUEST TO CORRECT TITLE OF INVENTION**

MAIL STOP ISSUE FEE  
COMMISSIONER FOR PATENTS  
P.O. BOX 1450  
ALEXANDRIA, VA 22313-1450

SIR:

In the matter of the above-identified application for patent, we hereby request  
correction of your records to reflect the correct title of the invention. The title of the  
invention should read as follows: SEMICONDUCTOR DEVICE **HAVING MULTI-  
LAYER INTERCONNECTION STRUCTURE** AND METHOD OF  
MANUFACTURING THE SAME.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.

  
Marvin J. Spivak

Registration No. 24,913

Joseph Scaretta, Jr.  
Registration No. 26,803

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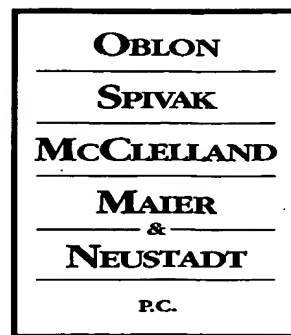
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(OSMMN 05/04)



DATE ALLOWED: June 1, 2005

COMMISSIONER FOR PATENTS  
ALEXANDRIA, VIRGINIA 22313



ATTORNEYS AT LAW

Docket No.: 240896US2S

RE: Application Serial No.: 10/628,372

Inventor: Naofumi NAKAMURA, et al.

Filing Date: July 29, 2003

For: SEMICONDUCTOR DEVICE HAVING MULTI-LAYER  
INTERCONNECTION STRUCTURE AND METHOD OF  
MANUFACTURING THE SAME

Group 2815

Examiner: WARREN, M. E.

SIR:

Attached hereto for filing are the following papers:

**LETTER SUBMITTING DRAWING REPLACEMENT SHEET(S)  
REPLACEMENT DRAWING SHEETS (10)**

Our check in the amount of \$0.00 is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R. 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.

  
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Docket No.: 240896US2S



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION: Naofumi NAKAMURA, et al.

SERIAL NO.: 10/628,372

GAU: 2815

FILED: July 29, 2003

EXAMINER: WARREN, M. E.

FOR: SEMICONDUCTOR DEVICE HAVING MULTI-LAYER INTERCONNECTION  
STRUCTURE AND METHOD OF MANUFACTURING THE SAME

**LETTER SUBMITTING REPLACEMENT DRAWING SHEET(S)**

COMMISSIONER FOR PATENTS  
Alexandria, VA 22313

SIR:

Responsive to the below indicated communication, the following drawing sheets are submitted herewith:

☒ 10 Replacement Drawing Sheets

☐ \_\_\_\_\_ New Drawing Sheets

☐ Official Action dated \_\_\_\_\_

☒ Notice of Allowance/Issue Fee dated June 1, 2005

☐ Other dated \_\_\_\_\_

The changes and/or modifications made include the following:

The replacement sheets include changes required by the Examiner's Amendment.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.

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